IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: David C. McClure

Serial No.: 10/066,182

Filed: January 31, 2002

For: CIRCUIT AND METHOD FOR TESTING A FERROELECTRIC MEMORY

DEVICE

To The Honorable Commissioner for Patents Washington, D.C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

Commissioner for Patents, Washington, D.C., 20231

on April <u>24</u>, 2002

Signature Linda M'Sweek

Dear Sir:

RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

In response to the Notice to File Corrected Application Papers mailed by the Application Branch on February 28, 2002, you will find enclosed four sheets of formal drawings. The formal drawing of Fig. 9 includes the change made thereto in the attached amendment.

PATENT APPLICATION

Docket No.01-C-050

It is respectfully submitted that no additional parts are required to be filed with regard to the above-referenced application, and the application should therefore be processed accordingly.

Respectfully submitted,

JENKENS & GILCHRIST, A Professional Corporation

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William F. Esser

Registration No. 38,053

1445 Ross Avenue, Suite 3200 Dallas, Texas 75202-2799 214/855-4181 214/855-4300 FAX



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APPLICATION NUMBER

FILING/RECEIPT DATE

FIRST NAMED APPLICANT

ATTORNEY DOCKET NUMBER

10/066,182

01/31/2002

David C. McClure

28940-00112USPT 01-C-050

CONFIRMATION NO. 2696

Date Mailed: 02/28/2002

FORMALITIES LETTER

OC000000007554474

Lisa K. Jorgenson, Esq. STMicroelectronics, Inc. 1310 Electronics Drive Carrollton, TX 75006-5039

COPY OF PAPERS ORIGINALLY FILED



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NOTICE TO FILE CORRECTED APPLICATION PAPERS

Filing Date Granted

his application has been accorded an Application Number and Filing Date. The application, however, is informal the center it does not comply with the regulations for the reason(s) indicated below. Applicant is given **TWO MONTHS** from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be estained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a)

The required item(s) identified below must be timely submitted to avoid abandonment:

• Substitute drawings in compliance with 37 CFR 1.84 because:

drawing sheets do not have the appropriate margin(s) (see 37 CFR 1.84(g)). Each sheet must include a top margin of at least 2.5 cm. (1 inch), a left side margin of at least 2.5 cm. (1 inch), a right side margin of at least 1.5 cm. (5/8 inch), and a bottom margin of at least 1.0 cm. (3/8 inch);

A copy of this notice MUST be returned with the reply.

Customer Service Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE





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Signature

Re:

Applicant(s):

David C. McClure

Serial No.

10/066,182

Filing Date: For:

January 31, 2002 CIRCUIT AND METHOD FOR TESTING A FERROELECTRIC

MEMORY DEVICE

Docket No.:

01-C-050

Dear Sir:

Transmitted for filing with the Patent and Trademark Office are the following documents for the above-referenced patent application:

- Response to Notice to File Corrected Application Papers
- Copy of Notice to File Corrected Application Papers 2.
- A red inked correction to Fig. 9
- Preliminary Amendment
- Four Sheets of Formal Drawings Figs. 1-9
- Patent Assignment with Assignment Cover Sheet
- A check in the amount of \$40.00 (fee according to CFR 1.21(h) for recording a patent 7. assignment
- 8. Acknowledgment Postcard

Please address all communications related to this to:

Lisa Jorgenson STMicrorelectronics, Inc. 1310 Electronics Drive Carrolton, TX 75006-5039

In the event there is an under or over payment, please debit or credit our Deposit Account #10-0447.

Respectfully submitted,

William F. Esser

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